



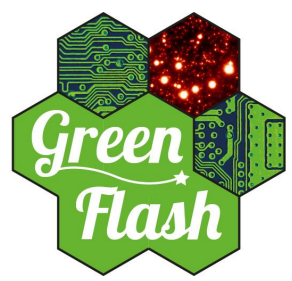
Green Flash

High performance computing for real-time science

WP 7 Status – PLDA / Accelize

Final Design Review – Meudon April 2018

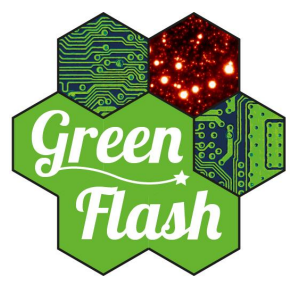




WP7.2 – Plans achievement

Feature	Status	Delivery / Notes
Arria 10 support	Done	QP dev version / XpressGXA10
DDR4 support	Done	QP dev version / XpressGXA10
[Peer to Peer] - FPGA to GPU Proof Of Concept	Done	Delivered in D5.1 (Proto 1)
[Ethernet sub-system] - UDP Multicast	Done	QP 2.1 / June 2016
[Ethernet sub-system] - Raw mode interface	Done	QP 2.2 / October 2016
GigE Vision preliminary support	Done	Delivered in D5.1 (Proto 1)

Feature	Status	Delivery / Notes
Aurora	Out of Scope	No need for OBSPM / MIC
NIC compliance - Preliminary support	Out of Scope	See WP5
Open MPI compliance - Preliminary support	Out of Scope	See WP5



WP7.2 – Plans update

Feature	ETA	Delivery / Notes
HMC support	Done	Smart Interconnect Proto #3
[Peer to Peer] - Generic support	Done	Smart Interconnect Proto #3
Arria 10 SOC support	Q3 2018	Smart Interconnect Proto #4
Support a RootPort implementation of the PCIe	Q4 2018	Smart Interconnect Proto #4
[Ethernet sub-system] - PTP 1588 support	Available	IP Core available at Microgate. Ethernet MAC provided by QuickPlay
[Ethernet sub-system] - down-scaling to 1G	Out of Scope	No real need identified up to now
[Ethernet sub-system] - up-scaling to 40G	Available	QSFP support for 4x10G, no IP core supporting 40G protocol
CameraLink	Descoped	Requires support for FMC daughter board in QP.
Open DDS compliance - Preliminary support	Q4 2018	